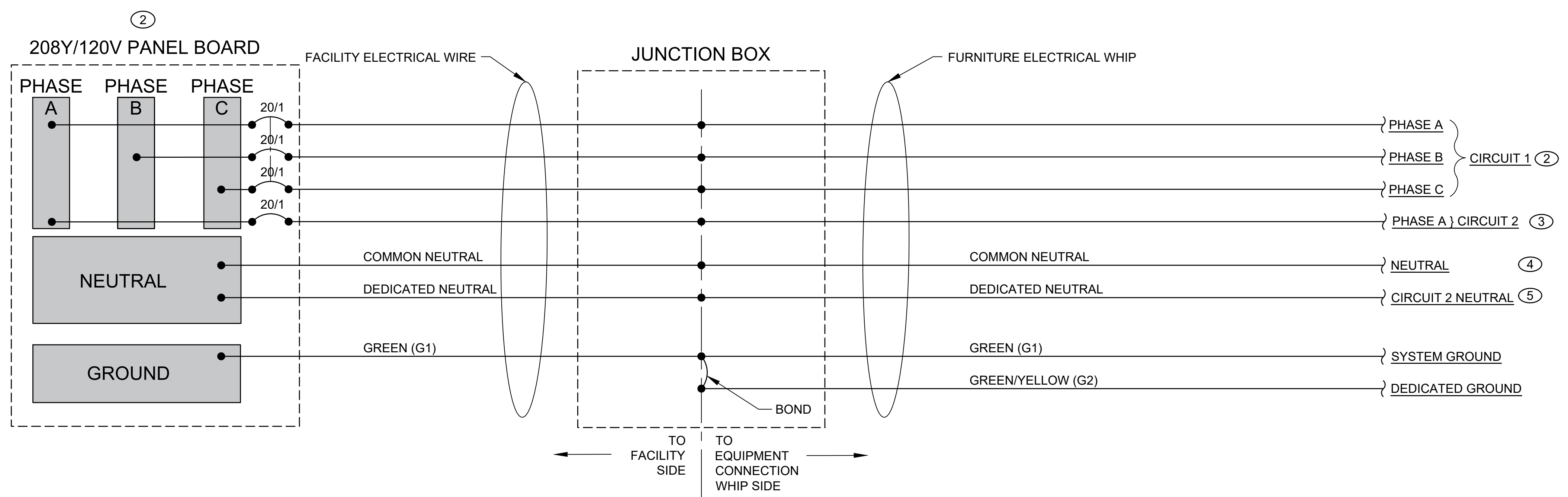


120/240V 3 WIRE SINGLE PHASE CIRCUIT PANEL DIAGRAM
SCALE: NONE



208Y/120V THREE PHASE CIRCUIT PANEL DIAGRAM
SCALE: NONE

NOTE: DESIGNER MUST EDIT DRAWINGS TO SUIT CONDITIONS.

GENERAL NOTES:

- LOCATE JUNCTION BOX WITHIN 4' OF FURNITURE CONNECTION.
- JUNCTION BOX WILL HOMERUN ALL CIRCUITS. 12 AWG FACILITY WIRING UNLESS NOTED OTHERWISE.
- ALL FURNITURE IS CONFIGURED FOR 8 WIRE SYSTEM. 3 SINGLE, 20 AMP CIRCUITS AS A MULTIWIRED BRANCH CIRCUIT AND ONE PHASE, NEUTRAL AND GROUND (FOR 4 CIRCUITS TOTAL).
- CUBICLE WIRING METHODS PER MANUFACTURER. CIRCUIT LENGTH FROM FACILITY CONNECTION TO FURTHEST DUPLEX RECEPTACLE SHOULD NOT EXCEED 150 FT (5% VOLTAGE DROP FOR A 20A CIRCUIT).
- MAXIMUM OF 8 GENERAL PURPOSE RECEPTACLES PER CIRCUIT (PER ESM). CONDUCTOR COLORS ON CONNECTION WHIP ARE FOR INFORMATION ONLY.
- FURNITURE/FACILITY CONNECTION SUPPORTS BOTH WALL AND CEILING MOUNTED JUNCTION BOX OPTIONS.
- PREFERENCE IS FOR ONE CIRCUIT PER CUBICLE.
- HERMAN MILLER'S GENERAL CONFIGURATION IS 3 MULTIWIRED BRANCH CIRCUITS AND ONE GENERAL PURPOSE CIRCUIT.
- REFERENCE ARTICLE 210.4 AND 210.5 OF THE NATIONAL ELECTRIC CODE.
- DISTRIBUTE CIRCUITS TO BALANCE LOAD ACROSS HOT CONDUCTORS CIRCUIT 1 AND 2 NEUTRAL AND CIRCUIT 3 AND 4 NEUTRAL.

KEYED NOTES:

- ALTERNATE HOT AS REQUIRED.
- MULTIWIRED BRANCH CIRCUIT WITH HANDLE TIE.
- 4TH CIRCUIT WITH A DEDICATED NEUTRAL. VARY 4TH CIRCUIT BETWEEN PHASES, AS REQUIRED.
- COMMON NEUTRAL FOR MULTIWIRED BRANCH CIRCUIT.
- DEDICATED NEUTRAL FOR 4TH CIRCUIT.
- NEUTRAL IS USED FOR BOTH LINE 1 AND 2

LBO-DESIGN PACKAGE REVIEWER	N/A			
APPROVED FOR RELEASE TORUCH SUBMITTED				
E.STROMBERG VERIFIED				
S.R.BACA DESIGNED				Posted 4/27/21
A.OLSEN DRAWN	N/A	0	INITIAL ISSUE FOR [DCF/DRN #]	MM/DD/YY
L.VALDEZ UNCLASSIFIED D.SMITH		NO	REVISION DESCRIPTION	DATE

ENGINEERING STANDARDS

ELECTRICAL

HERMAN MILLER FURNITURE WIRING DIAGRAM

TA-XX BLDG XXXX SHEET **E-2**

Los Alamos NATIONAL LABORATORY P.O. Box 1663 Los Alamos, New Mexico 87545 **1** OF **1**

PROJECT ID: **CHAPTER 7** DRAWING NO: **ST-D5020-2.2** REV: **0**